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APPLICANTS Takashi Hoshino, Osaka-shi, Osaka, JAPAN; Shin Harada, Osaka-shi Osaka, JAPAN; Kazuhiro Fujikawa, Osaka-shi Osaka, JAPAN; Satoshi Hatsukawa, Osaka-shi Osaka, JAPAN; Kenichi Hirotsu, Osaka-shi Osaka, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/09412 07/24/2003				
** FOREIGN APPLICATIONS ***** JAPAN 2002-215804 07/24/2002 JAPAN 2002-235045 08/12/2002				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged <u> </u> Examiner's Signature <u> </u> Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 48	TOTAL CLAIMS 47
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TITLE VERTICAL JUNCTION FIELD EFFECT TRANSISTORS, AND METHODS OF PRODUCING THE VERTICAL JUNCTION FIELD EFFECT TRANSISTORS				
FILING FEE RECEIVED 6160	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	